# G486VPB

# Rev. 0+ System Board User's Manual

-D28750815-

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# FCC Statement on Class B

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and the receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio TV technician for help.

## Notice:

- 1. The changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.
- 2. Shielded interface cables must be used in order to comply with the emission limits.

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# Introduction

The G486VPB system board offers several advanced features integrated into the system board. Its design is based on the new Peripheral Component Interconnect (PCI) local bus and Industry Standard Architecture (ISA) standards.

The G486VPB is an Energy Star compliant system board that supports the Environmental Protection Agency's Green PC requirements. It reduces energy consumption by automatically turning off peripherals when not in use.

The G486VPB system board supports a 237-pin Zero Insertion Force (ZIF) CPU socket that allows users to easily upgrade their CPUs. It also supports an optional Flash EPROM for easy BIOS upgrades. Flash EPROM is a memory chip for the storage of BIOS which can be erased in bulk or modified using a software utility.

The G486VPB is equipped with four PCI slots, four ISA slots (one shared PCI/ISA slot).

Equipped with one built-in VL-bus IDE connector and one ISA IDE connector, the G486VPB system board can control four drives and supports fast Mode 4 hard drives and hard drives larger than 528MB under DOS. The system board is also equipped with two serial ports, an ECP/EPP parallel port, a floppy disk drive controller, and one optional mini-DIN-6 connector for the PS/2 mouse.

The G486VPB can support 1MB to 128MB of memory using 256Kx36, 512Kx36, 1Mx36, 2Mx36, 4Mx36, 8Mx36 and 16Mx36 72-pin SIM modules.

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# Features and Specifications

## **Processor**

• 5V CPUs

Intel 486SX/DX/DX2 (full series) and Pentium<sup>™</sup> OverDrive Processor AMD 486DX/DXL/DXL2 (full series)

Cyrix 486SX/DX/DX2 (full series)

• 3.3V CPUs

Intel 486DX4-75/100 AMD 486DX2-66/80NV8T 486DX4-75/100NV8T 486DX2-66/80 SV8B 486DX4-75/100 SV8B Cyrix 486DX2V-50/66/80 486DX4-100 5x86-100

## Chipset

 VIA - VT82C496G - system and cache controller VT82C505 - Host to PCI bridge VT82C406 - peripheral chip

## **Power Management**

- Supports System Management Mode (SMM) and CPU stopped clock
- Hardware provides flexible event-driven power management scheme and easy CPU transition state: Ready, Doze and Suspend
- Microsoft APM 1.1 compliant
- EPA Energy Star compliant design

### PCI/ISA System Board-

#### BIOS

- Award system BIOS (Flash EPROM optional)
- Supports IDE HDDs larger than 528MB under DOS (LBA mode only)
- System and EGA/VGA BIOS shadow

## Super VO Interface and Fast IDE

- Two NS16C550 compatible serial ports
- One parallel port with EPP and ECP support
- Supports 360K, 720K, 1.2M, and 1.44MB floppy drives
- Two fast IDE connectors:

Primary IDE connector provides a VL-bus IDE interface to support up to two Mode 4 enhanced HDDs

Secondary IDE connector provides an ISA IDE Interface to support an ATAPI compliant CD-ROM or a tape drive

## Cache Memory

- 128KB, 256KB, or 512KB
- Supports Level 1 and Level 2 write-back/ write-through cache

## Memory Onboard

- 1MB to 128MB
- Two 72-pin SIMM sockets
- Supports memory parity check function
- Supports 256KBx36, 512KBx36, 1MBx36, 2MBx36, 4MBx36, 8MBx36 and 16MBx36 SIMMs

## **Expansion Slots**

- Three PCI slots
- One shared PCI/ISA slot
- Three 16-bit ISA slots

## **CPU Socket**

• 237-pin ZIF socket (Intel Socket 3)

## **Board Dimensions**

• 22cm (8.58") x 22cm (8.58") Baby AT form factor

## PCB

• 4 layers

# Package Checklist

The G486VPB package contains the following items:

- The G486VPB system board
- The G486VPB user's manual
- One IDE driver diskette
- One 34-pin floppy disk drive cable
- Two 40-pin IDE hard disk cables
- One 25-pin printer port cable for chassis mounting
- One card-edge bracket with serial and mouse port cables

If any of these items are missing or damaged, please contact your dealer or sales representative for assistance.





# Installation Overview

This chapter summarizes the steps in installing the G486VPB system board into your system unit.

# Preparing the Area

Before unpacking the system board, make sure the location you have selected is relatively free of dust and static electricity. Excessive exposure to dust, static electricity, direct sunlight, excessive humidity, extreme cold, and water can damage the operational capabilities of your system board. Avoid placing the unit on soft surfaces such as beds and carpeted floors which can hinder air circulation. These areas also attract static electricity which can damage some circuits on your system board.

Make sure that the power source has a properly grounded, three-pronged socket. It is essential that the power connection be properly grounded for correct functioning of your system board. For further protection, we recommend that you use a surge suppressor. This will protect the system board from damage that may result from a power surge on the line.

Move items that generate magnetic fields away from your system board, since magnetic fields can damage your system board. Once you have selected the ideal location, unpack the G486VPB system board carefully.

# Handling the System Board

It is quite easy to inadvertently damage your system board even before installing it to your system unit. Static electrical discharge can damage computer components without causing any signs of physical damage. You must take extra care in handling the system board to ensure against electrostatic build-up.

## Tips in Handling the System Board

- 1) To prevent electrostatic build-up, leave the board in its anti-static bag until you are ready to install it.
- 2) Wear an antistatic wriststrap.
- 3) Do all preparation work on a static-free surface with components facing up.
- 4) Hold the system board by its edges only. Be careful not to touch any of the components, contacts or connections, especially gold contacts on the board.
- 5) Avoid touching the pins or contacts on all modules and connectors. Hold modules and connectors by their edges.

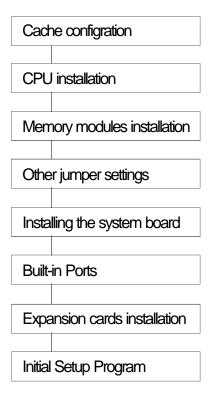
## Warning:

Electrostatic discharge (ESD) can damage your upgrade processor, disk drives, add-in boards, and other components. Perform the upgrade instruction procedures described at an ESD workstation only. If such a station is not available, you can provide some ESD portection by wearing an antistatic wrist strap and attaching it to a metal part of the system chassis. If a wrist strap is unavailable, establish and maintain contact with the system chassis throughout any procedures requiring ESD protection.



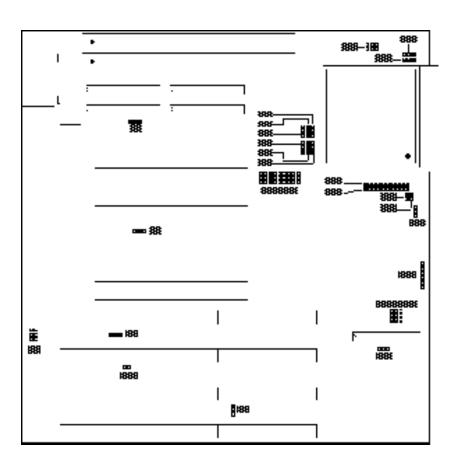
# Hardware Installation

# **Installation Procedures**





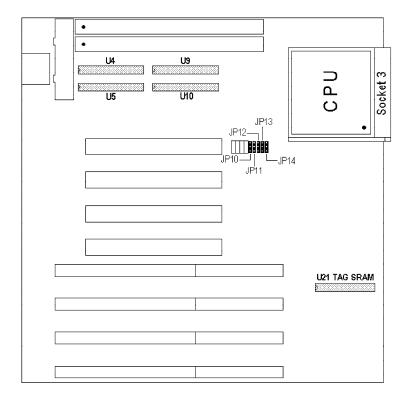




Locations of Jumpers on the G486VPB

# Cache Configuration

The G486VPB system board can be configured to three different cache sizes: 128KB (default size), 256KB, and 512KB. Regardless of the amount of cache memory installed, one 8Kx8 or 32Kx8 is needed for tag RAM to store the cacheable addresses. The locations of the SRAM sockets and Jumpers JP10-JP14 on the system board are shown below.



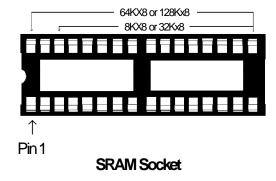
Locations of the SRAM Sockets and Jumpers JP10-JP14 on the G486VPB System Board



To upgrade from 256KB to 512KB of cache memory, use four pieces of 128Kx8 SRAM.

Cache Size	Data SRAM	Tag SRAM	Cacheable
	U4/U5/U9/U10	U21	Range
512KB	128Kx8x4	32Kx8x1	128MB
256KB	64Kx8x4	32Kx8x1	64MB
128KB*	32Kx8x4	8Kx8x1	32MB

<sup>\*</sup> Default setting



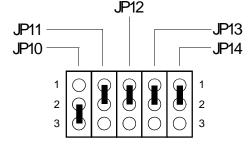




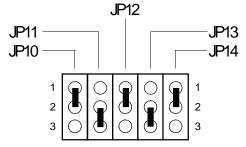
# **Jumper Setting for Cache Memory**

Please refer to the figure on page 14 for the locations of Jumpers JP10, JP11, JP12, JP13 and JP14.

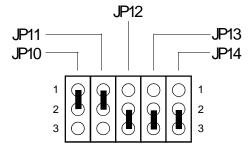
# 128KB\* Cache SRAM (Default)



## 256KB Cache SRAM



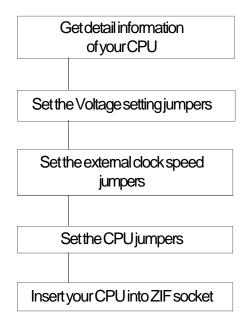
## 512KB Cache SRAM



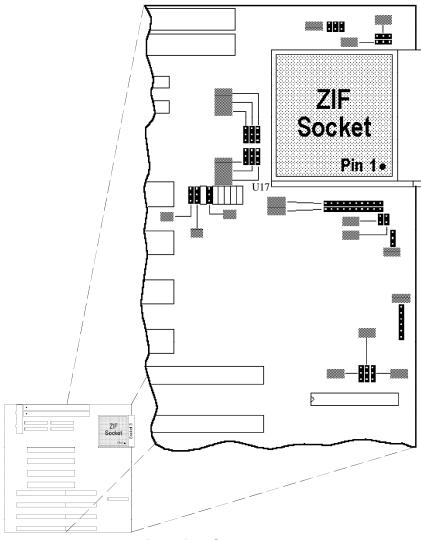
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## **CPU Installation**



The G486VPB is equipped with a 237-pin Zero Insertion Force (ZIF) socket at location U17 (please refer to the next page) of the system board. This socket is designed for the easy removal of the old CPU and the easy insertion of the upgrade CPU. If you need to apply excessive force to insert the CPU, the installation is being done incorrectly. Make sure the jumpers are set correctly before applying power, or you may damage the CPU or system board.



Locations of ZIF Socket, Jumpers JP6 - JP7, JP9 , JP15 - JP20, JP21, JP23 - JP30 and JRN1 - JRN3 on the G486VPB System Board

# **Jumpers JP21**, **JP23** and **JP24** CPU Voltage Select

## 5VCPUs\*

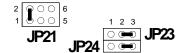


**JP21** Offand **JP23, JP24** 1-2 On: 5V (Default)

5V CPUs:

Intel 486SX/DX/DX2
SX-S/DX-S/DX2-S
DX4 OverDrive
Pentium OverDrive
AMD Am486DX/DXL/DXL2
Cyrix 486SX/DX/DX2
SGS Thompson ST486DX2

## **3.45VCPUs**



**JP21** 1-2 On and **JP23, JP24** 2-3 On: 3.45V

3.45V CPUs:

Intel 486DX4

AMD Am486DX2/DX4 NV8T Am486DX2/DX4 SV8B

UMC U5SD

Cyrix 486DX2-V50

486DX4-100

5x86-100

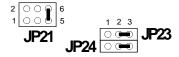
# 3.6VCPUs



**JP21** 3-4 On and **JP23, JP24** 2-3 On: 3.6V

3.6V CPUs: Cyrix 486DX2-V66

## 4VCPUs



**JP21** 5-6 On and **JP23, JP24** 2-3 On: 4.0V

4V CPUs:

Cyrix 486DX2-V80

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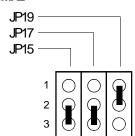
## PCI/ISA System Board-

## **Jumpers JP15, JP17 and JP19** CPU External Clock Selection

# 25MHz JP19 JP17 JP15 1 2 3

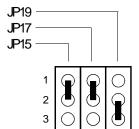
JP152-3On, JP171-2On and JP192-3On: 25MHz

## 33MHz\*



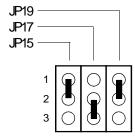
JP152-3On, JP172-3On and JP191-2On: 33MHz(Default)

#### 40MHz



**JP15**1-2On, **JP17**1-2On and **JP19**2-3On: 40MHz

## 50MHz

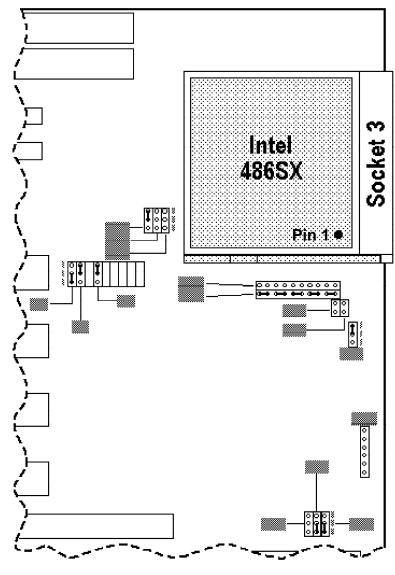


**JP15**1-2On, **JP17**2-3On and **JP19**1-2On: 50MHz

External CPUClockSpeed	25MHz	33MHz*	40MHz	50MHz
Internal	25/50MHz	33/66/100MHz	40/80MHz	50MHz
CPU Clock Speed				
Pentium OverDrive	63MHz	83MHz		
Internal Clock Speed				

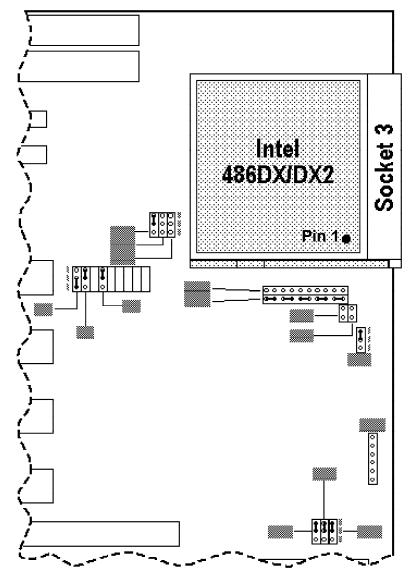
- G486VPB

# Jumper Settings for CPU Type Select Intel 486SX



PCI/ISA System Board -

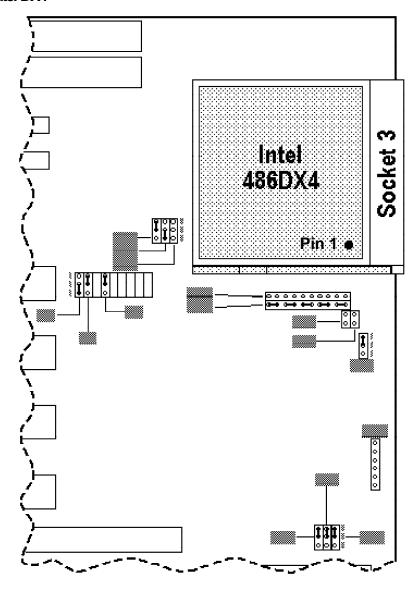
# Intel 486DX/DX2 \* (Default)



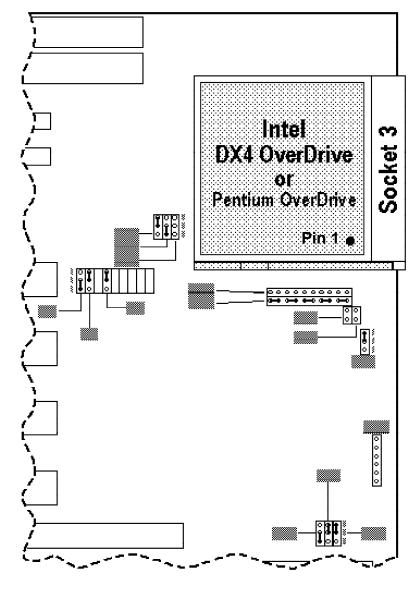
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- G486VPB

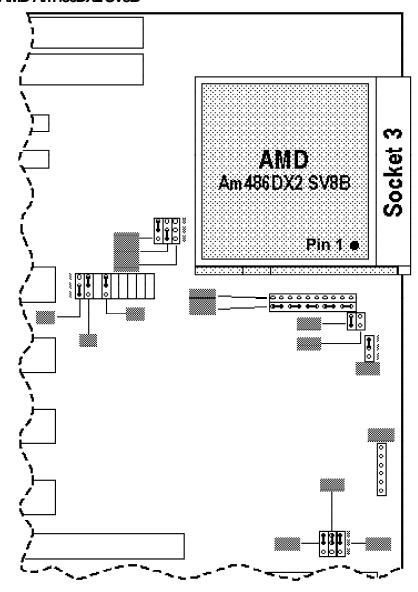
# Intel DX4



## Intel DX4 OverDrive / Pentium OverDrive

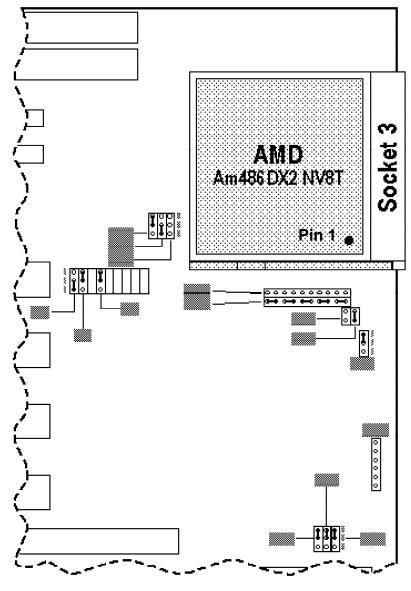


# AMD Am486DX2 SV8B



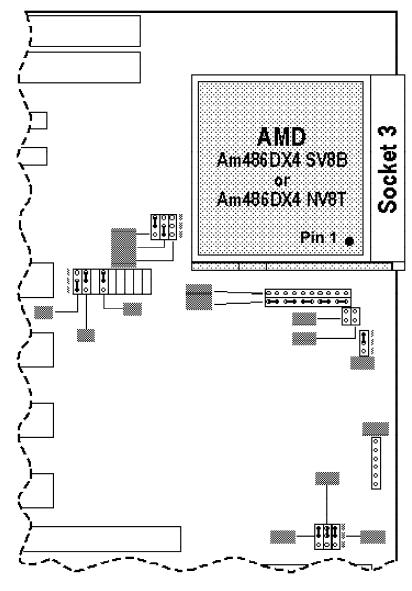
PCMSA System Board -

## AMD Am486DX2 NV8T



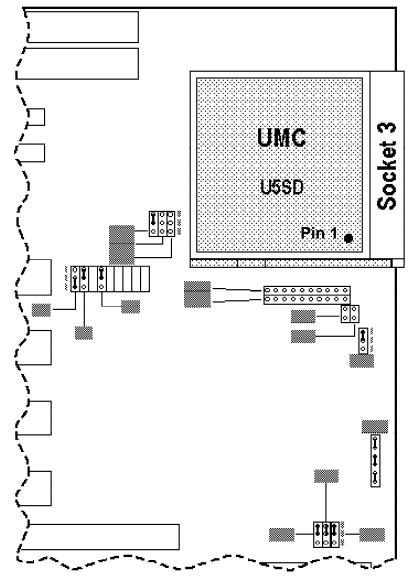
G486VPB

## AMD Am486DX4 SV8B / Am486DX4 NV8T



PCI/ISA System Board-

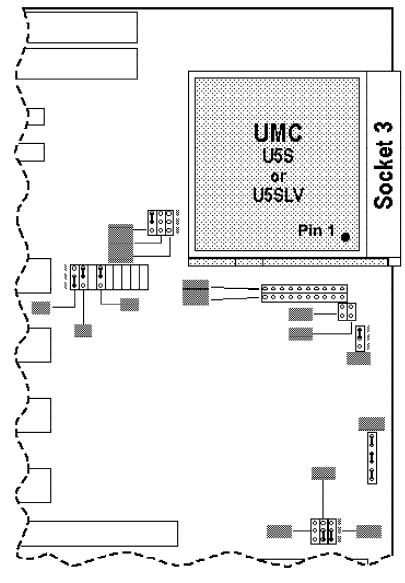
# UMC U5SD



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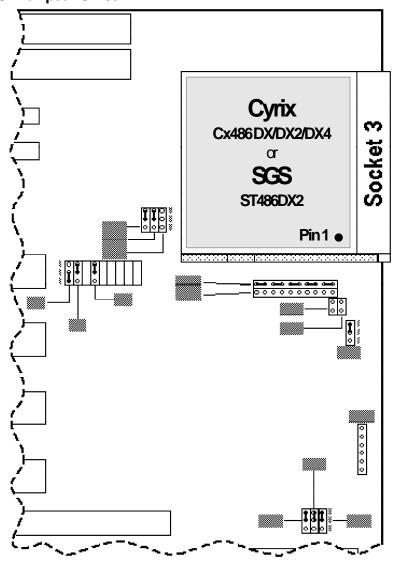
- G486VPB

# UMC U5S/U5SLV



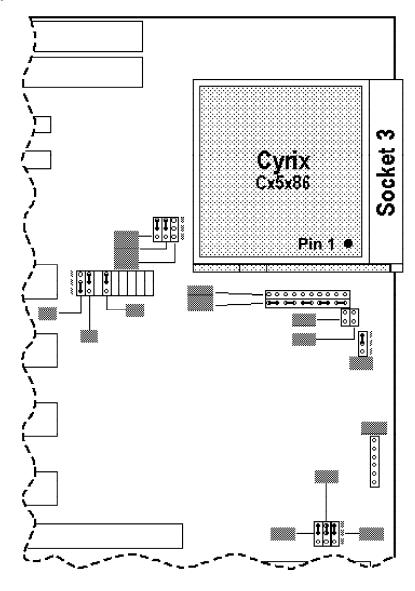
PCI/ISA System Board -

# Cyrix Cx486 DX/DX2/DX4 SGS Thompson ST486DX2



- G486VPB

# Cyrix Cx5x86



## Installing Upgrade CPUs

The G486VPB is equipped with a 237-pin Zero Insertion Force (ZIF) socket at location U17 of the system board. This socket enables you to remove the old CPU and insert the upgrade CPU easily. If you need to apply excessive force to insert the CPU, the installation is being done incorrectly.

## Warning:

Open the socket only if actually installing a CPU. The warranty on the original CPU will be voided if the S/N seal is broken. Installation of an OverDrive Processor will not affect the original system warranty.

The 237-pin ZIF socket consists of the four rows of pin holes on each side. To prevent improper OverDrive Processor installation, the ZIF socket has a Plug/Keying mechanism. Several holes in the socket are plugged so OverDrive Processors will go in only one way. If you cannot easily insert the OverDrive Processor, verify that pin 1 of the CPU is aligned with pin 1 of the socket. A warning note--be extremely careful to match pin 1 of the CPU with Pin 1 of the socket. Only Intel's OverDrive Processor is keyed to prevent improper placement in the ZIF socket. Other Intel CPUs, as well as CPUs from other vendors, *can* be placed incorrectly and *will* be permanently damaged if placed incorrectly.

#### **Caution:**

You will need to change the CPU-type jumper settings. (Please see the previous pages of this manual.)

Before proceeding with the upgrade, take note of the following. The microprocessor and heat sink may be hot

if the system has been running. To avoid the possibility of a burn, power the system off and let the processor and heat sink cool for 10 minutes.

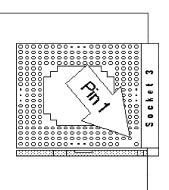
## Clearance Requirements

The Pentium OverDrive processor comes with a heat sink mounted on top. To maintain proper airflow once the upgrade is installed on the system board, the processor and heat sink require certain space clearances.

The clearance above the OverDrive processor's fan/heat-sink must be at least 0.4 in. The clearance on at least 3 of 4 sides of the processor must be at least 0.2 in. The cables (for floppy drive, hard drive, CD-ROM, etc.) must be routed clear of the CPU and its airspace.

## Temperature Rating

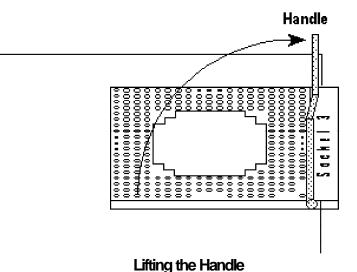
The ambient or room temperature must be below 37°C (99°F) for a system installed with the Pentium OverDrive processor.



Zero Insertion Force (ZIF) Socket

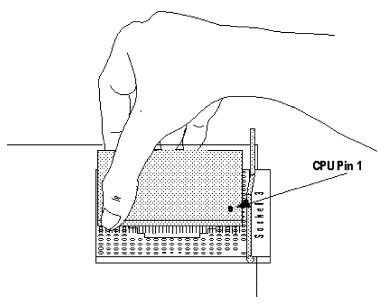
To install an upgrade CPU in the G486VPB, follow the steps below.

1. Make sure the handle on the side of the ZIF socket is up. To raise the handle, pull it out to the side a little and raise it as far as it will go. The top plate will slide back. Do not use screwdrivers or other tools to open the socket, or damage may occur to the system or socket. It may be necessary to initially apply a small amount of sideways force to free the handle from its retaining "tab." Once clear of the "tab," the lever will open easily.



2. Once the lever is completely up, remove the old CPU by carefully lifting it straight out of the socket. You are now ready to insert the new CPU.

- 3. Position the CPU above the ZIF socket. Make sure pin 1 of the CPU is aligned with pin 1 of the socket. Lower the chip until the pins are inserted properly in their corresponding holes. Remember that no force is needed to install the CPU. If the CPU will not easily insert, please verify you have pin 1 of the CPU aligned with pin 1 of the socket. Applying too much pressure can damage the CPU or the socket.
- 4. Push the handle down until the handle locks into its place. The top plate will slide forward. You will feel some resistance as the pressure starts to secure the CPU in the socket. This is normal and will not damage the CPU. However, if the handle is not completely closed, damage to the CPU and/or system board may result.



Positioning the CPU Above the ZIF Socket

## PCMSA System Board -

## Fan Exhaust

The CPU must be kept cool by using a fan exhaust configuration in connection with the heatsink. The temperature of the air entering the fan/heatsink cannot exceed 45°C.

In order to provide proper airflow to the CPU, all movable obstructions (power supply cables, cards, floppy disk cables) must be clear of the CPU heatsink/fan component in accordance with the space clearance discussed in the CPU installation section of this manual.

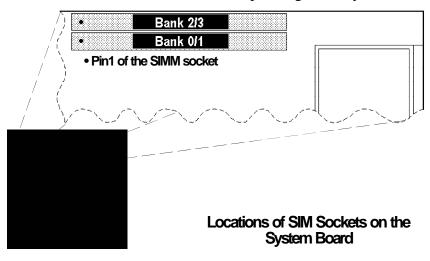
# Memory Installation

The G486VPB system board can support 1MB to 128MB of memory using HSIM modules. HSIM is an acronym for High Density Single In-line Memory.

A SIM module consists of several RAM chips soldered onto a small circuit board. A SIM module connects to the system board via a 72-pin card-edge connector.

The SIM sockets are divided into four banks on the system board. The G486VPB uses 256Kx36, 512Kx36, 1Mx36, 2Mx36, 4Mx36, 8Mx36, or 16Mx36 HSIM modules installed in four banks. Non-parity (x32) SIMMs can also be used if parity checking is turned off in CMOS Setup.

You will need 1 or 2 pieces of SIM modules, depending on the amount of memory you intend to install. The following table summarizes the bank locations and modules needed for the corresponding memory sizes.



## PCI/ISA System Board -

Memory Size	Bank0/1	Bank2/3
1MB	<b>1</b> M	-
2MB	1M	<b>1</b> M
4MB	<b>4</b> M	-
5MB	<b>4</b> M	<b>1</b> M
8MB	<b>4</b> M	<b>4</b> M
16MB	16M	-
17MB	16M	1M
20MB	16M	<b>4</b> M
32MB	16M	16M
64MB	64M	-
65MB	64M	1M
68MB	64M	<b>4</b> M
128MB	64M	64M
2MB	2M(D)*	-
3MB	2M(D)*	1M
4MB	2M(D)*	2M(D)*
6VB	2M(D)*	<b>4</b> M
10MB	2M(D)*	8M(D)*
18MB	2M(D)*	16M
8VB	8M(D)*	-
9MB	8M(D)*	1M
12MB	8M(D)*	<b>4</b> M
16MB	8M(D)*	8M(D)*
24MB	8M(D)*	16M
72MB	8M(D)*	64M
32MB	16M(D)*	16M(D)*
32MB	32M(D)*	-
33MB	32M(D)*	1M
34MB	32M(D)*	2M(D)*
36MB	32M(D)*	4M
40MB	32M(D)*	8M(D)*
48MB	32M(D)*	16M
64MB	32M(D)*	32M(D)*
96MB	32M(D)*	64M

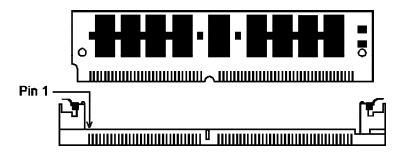
<sup>\*(</sup>D)-Double Density

#### Note:

If you are installing only one SIMM, you can install it in any of the sockets. The bank you choose is considered to be Bank 0/1.

## Installing the Modules

An HSIM module simply snaps into a socket on the system board. Pin 1 of the HSIM module must correspond with Pin 1 of the socket.

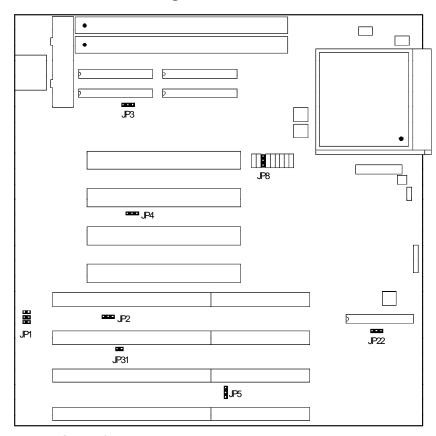


- 1. Position the HSIM module above the socket with the "notch" in the module aligned with the "key" on the socket.
- 2. Seat the module at a 45° angle into the bank. Make sure it is completely seated. Tilt the module upright until it locks in place in the socket.



## Other Jumper Settings

Jumper Settings for Internal/External Battery, CMOS Clean, Flash EPROM, PS/2 Mouse, CPU Speed for PCI Slots and Parallel Port ECP DMA Channel Setting



Locations of Jumpers JP1, JP2, JP3, JP4, JP5, JP22 and JP31 on the G486VPB System Board

## Jumper JP3

Internal Battery Select

The G486VPB comes with an internal Lithium battery. If you wish to install an external 3.6V Lithium battery, set JP3 pins 1 and 2 to On.





1-2 On: External Battery Enabled (Default) 2-3 On: Internal Battery Enabled

#### **Note:**

If you enable the external battery, you must connect the external battery to connector J3 or your system will lose its CMOS settings when powered off.

#### Jumper JP22 CMOS Clean Select

If, for some reason, the CMOS becomes corrupted, the system can be reconfigured with the default values stored in the ROM BIOS. To load the default values, set JP22 pins 2 and 3 to On for approximately two minutes, then return to the default position.



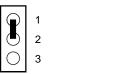
1 2 3

1-2 On: Normal (Default) 2-3 On: CMOS Clean

#### PCI/ISA System Board

## Jumper JP5 **EPROM Type**

Jumper JP5 shoud be set to match the type of the EPROM installed on the system board. The EPROM type setting has been set by the manufacturer; do not change the setting by yourself without manufacturer authorization.



2

1-2 On: 12V Flash EPROM28F010 (Default)

2-3 On: 5V Flash EPROM 29F010 or EPROM27C010

#### Jumpers JP2 and JP31 PS/2 Mouse

The G486VPB package includes a card-edge bracket with serial and mouse port. By default, the mouse port is disabled. If you intend to use a PS/2 mouse, the port must be enabled. The PS/2 mouse port uses IRQ12, but when the PS/2 mouse port is disabled, IRQ12 is available to other devices. To enable the PS/2 mouse, set Jumper JP2 pins 1 and 2 to On, and JP31 to Off. Make sure you connect the PS/2 mouse port to Connector CN3.

**JP2** 2-3 On and **JP31** On: PS/2 Mouse Disabled (Default)

JP2 1-2 On and JP31 Off: PS/2 Mouse Enabled

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## Jumpers JP4 and JP8 CPU External Clock Speed for PCI Slots

These jumpers should be configured according to the type of CPU installed on the system board. Set Jumpers JP4 and JP8 pins 2 and 3 to On if the CPU external clock speed is greater than 33MHz. If the CPU external clock speed is less than or equal to 33MHz, set Jumpers JP4 and JP8 pins 1 and 2 to On.





1-2 On: CPU External Clock Speed <= 33MHz (Default)

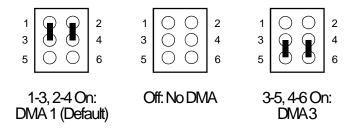
2-3 On: CPU External Clock Speed > 33MHz

Set Jumpers JP4 and JP8 pins 2-3 to On if your G486VPB system board uses one of the following CPUs: 486DX-50, 486DX2-80, 486DX-40 or 486SX-40. The CPU external clock speed supported by each of them is greater than 33MHz.

#### PCI/ISA System Board-

#### **Jumper JP1** ECP DMA Channel Select

The on-board printer port can be set to ECP mode in CMOS Setup. In ECP mode (but not in the other printer port modes), the printer port requires a DMA channel. Set Jumper JP1 to assign either DMA Channel 1 or DMA Channel 3 to the ECP port. Use a DMA channel that does not conflict with your add-in cards. For example, if you have a sound card that uses DMA3, then assign DMA1 to the ECP port.



# Installing the System Board

Before installing the system board into your system unit, you should prepare the tools you will need:

You will need:

- one medium size, flat-bladed screwdriver
- one medium Phillips screwdriver
- one grounded wriststrap for electrostatic discharge protection

## Step 1

Unlock your system unit. Turn off the power and disconnect all power cords and cables.

## Step 2

Remove the system unit cover. Refer to the manufacturer's instructions if necessary.

## Step 3

Wear the grounded wriststrap, remove all expansion cards and detach all connectors from the old system board.

## Step 4

Loosen the screws holding the original system board and remove the board from the system. Save the screws.



#### PCI/ISA System Board-

## Step 5

Remove the G486VPB from its original packing box. Be careful to avoid touching all connectors and pins on the board. Please refer to the handling instructions on pages 10 and 11 for proper handling techniques.

## Step 6

Insert the HSIM modules into the SIMM banks on the G486VPB. The quantity and location of the SIM modules is dependent upon the memory configuration and type of modules you intend to use.

## Step 7

Install the CPU. Be sure Pin 1 of the CPU is aligned with Pin 1 of the socket.

## Step 8

Set the appropriate jumpers.

## Step 9

Install the prepared G486VPB system board into the case and replace the screws.

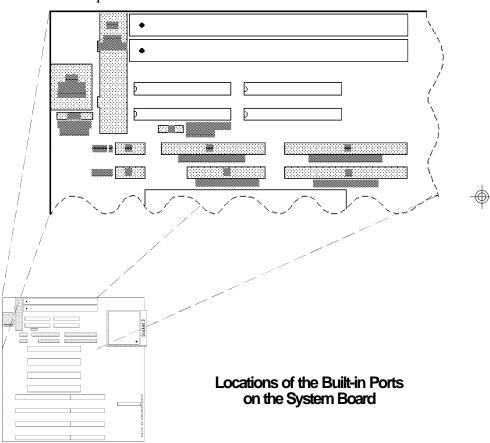
# Step 10

Reinstall all cards and connectors and replace the system unit cover. Reconnect all power cords and cables.

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# **Built-in Ports**

The G486VPB system board is equipped with two serial ports, one ECP/EPP parallel printer port, one FDD connector, two IDE hard disk connectors and one PS/2 mouse port.



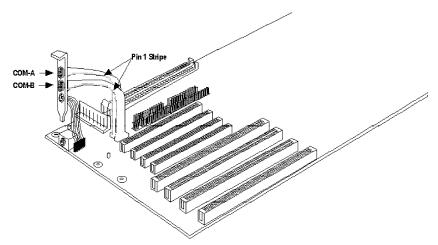
## Serial Ports

The built-in serial ports are RS-232C asynchronous communication ports with 16C550-compatible UARTs that can be used with modems, serial printers, remote display terminals, and other serial devices. The G486VPB system board supports IRQ4 for the primary serial port and IRQ3 for the secondary serial port. These serial ports use the following system I/O addresses:

Port Configuration	VO Address
00M1	3F8h
00M2	2F8h
00M3	3E8h
00M4	2E8h

## Connecting the Serial Ports

Two DB-9P serial port cables are provided with the motherboard. They are mounted on a card-edge bracket along with the PS/2 mouse cable. The upper serial port cable should be used for the COM1 primary serial port; connect it to Connector J1 on the motherboard. The lower serial port cable should be used for the COM2 secondary serial port; connect it to Connector J2 on the motherboard. Make sure the colored stripes on the ribbon cables align with pin 1 of Connectors J1 and J2. Mount the card-edge bracket to the system chassis.



The Primary and Secondary Serial Ports on the G486VPB System Board

## Parallel Port

The G486VPB system board has a standard connector for interfacing your PC to a parallel printer. This port is compatible with both IBM AT printer ports and the new, high speed, bi-directional Extended Capabilities Port standard. The parallel port on your system board can be set to any of the following system I/O addresses:

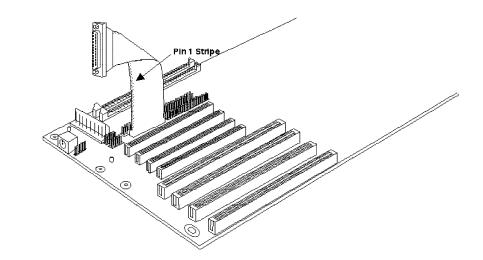
Port Configuration	VO Address
LPT1	3BC-3BE Hex
LPT2*	378-37A Hex
LPT3	278-27A Hex

<sup>\*</sup> Default setting

#### PCI/ISA System Board-

## Connecting the Parallel Printer Port

Attach the DB-25S printer port cable, which came with the motherboard, to Connector J5 on the G486VPB system board. Make sure the colored stripe on the ribbon cable aligns with pin 1 of Connector J5. Use a small nutdriver to mount the cable into a DB-25 cutout in the system chassis.

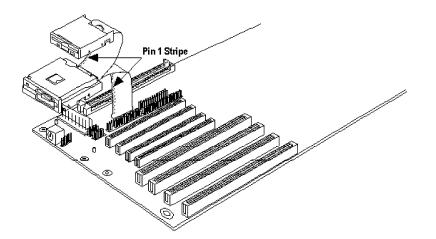


The Parallel Printer Port on the G486VPB System Board

# Floppy Disk Drive Controller

The G486VPB system board has a built-in floppy disk controller that supports two standard floppy disk drives. You can install any 360KB/720KB/1.2MB/1.44MB floppy disk drives (2.88MB optional).

## Connecting the Floppy Disk Cable



# The Floppy Disk Connector on the G486VPB System Board

## Step 1

Install the 34-pin header connector into the floppy disk connector (J4) on the system board. The colored edge of the ribbon should be aligned to pin 1 of the J4 connector.

#### PCI/ISA System Board-

#### Step 2

Install the other 34-pin header connector(s) into the disk drive(s) with the colored edge of the daisy chained ribbon cable aligned to pin 1 of the drive edge connector(s). The end-most connector should be attached to the drive you want to be Drive A.

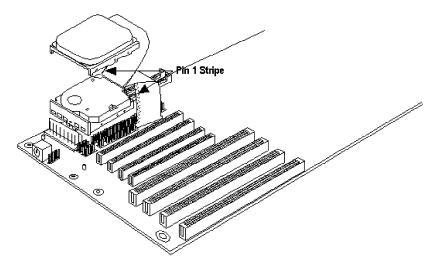
## IDE Hard Disk Interface

The G486VPB system board is equipped with one VL-Bus IDE connector and one ISA IDE connector that will interface four IDE (Integrated Drive Electronics) hard disk drives. An IDE drive is a hard drive with the controller electronics built into the disk assembly. The integration of the controller and the drive as a single unit increases both the reliability and performance by eliminating redundant circuitry.

#### Note:

Only IDE drives can be connected to the IDE interface.

## Connecting the IDE Hard Disk Interface



# The IDE Hard Disk Connector on the G486VPB System Board

## Step 1

If you are connecting two hard drives, install the 40-pin connector of the IDE cable into the primary disk connector (J7). If you are adding a third or fourth IDE device, install the 40-pin connector of the other IDE cable into the secondary connector (J6). Make sure the colored edge of the ribbon is aligned to pin 1.

#### Step 2

Install the other 40-pin header connector(s) into the device with the colored edge of the ribbon cable aligned to pin 1 of the drive edge connector(s).

#### PCI/ISA System Board-

#### **Note:**

Refer to your disk drive user's manual for information about selecting proper drive switch settings.

## Adding a Second IDE Hard Drive

When using two IDE drives, one must be set as the master and the other as the slave. Follow the instructions provided by the drive manufacturer for setting the jumpers and/or switches on the drives. No changes are needed on the G486VPB system board when adding a second hard drive.

We recommend that the IDE hard drives be from the same manufacturer. In a few cases, drives from two manufacturers will not function properly when used together. The problem lies in the hard drives, not the G486VPB system board.

## Preparing an IDE Drive for Use

IDE disk drives are already low-level formatted, with any bad-track errors entered, when shipped by the drive manufacturer. Do not attempt to do a low-level format, or you may cause serious damage to the drive.

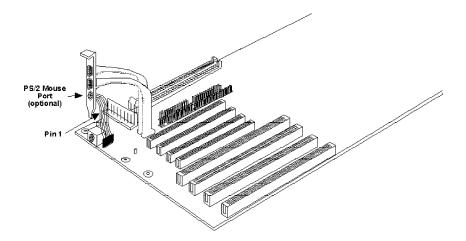
To use an IDE drive, you need to enter the drive type (this information is provided by the drive manufacturer) into the system's CMOS setup table. Then run FDISK and FORMAT provided with DOS.

#### Note:

Do not run FDISK and FORMAT programs on a drive that has already been formatted, or you will lose all programs and data stored on the drive.

## PS/2 Mouse Port

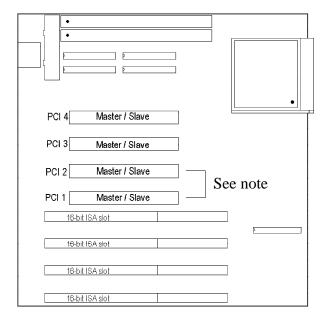
The PS/2 mouse port is a 6-pin connector on the system board. Attach the 6-pin mouse port cable, which came in the G486VPB box, to Connector CN3. Make sure the brown wire on the PS/2 mouse connector aligns with pin 1 of connector CN3. The other end of the wires are attached to the card-edge bracket that also holds the two serial port cables.



The PS/2 Mouse Port on the G486VPB System Board

# Installing Expansion Cards

The G486VPB is equipped with three dedicated PCI and three dedicated ISA slots. One PCI slot and one ISA slot is shared. You can only install a card in one or the other of the shared slots at a time, you cannot install devices in both slots. Due to the size of the CPU and heatsink/fan component, card size is limited. The location of the expansion slots are shown below.



#### Note

The system can support three bus-master PCI cards. Place two bus-master cards in slots PCI3 and PCI4. A third bus-master card can be placed in either slot PCII or PCI2. The remaining slot (PCII or PCI2) can support a slave card only.

# Initial Setup Program

After you power up your system, the BIOS message appears on your screen and the memory count begins.

After the memory test, the following message will appear on the screen:

### Press DEL to enter Setup

If the message disappears before you respond, restart your system or press the "Reset" button. You may also restart the system by pressing the <Ctrl> <Alt> <Del> keys. If you do not press these keys at the correct time and the system does not boot, the following error message will appear:

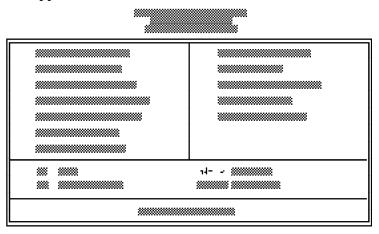
#### Press Del to enter Setup

If you have set a password and selected "System" in the Security Option of the BIOS Feature Setup menu, you will be prompted for the password every time the system is rebooted or any time you try to enter Setup. Type in the correct password and press <Enter>.

If you selected "Setup" in the Security Option, you will be prompted for the password only when you try to enter Setup. Refer to the "BIOS Features Setup" section for more information.

# Award BIOS CMOS Setup Utility

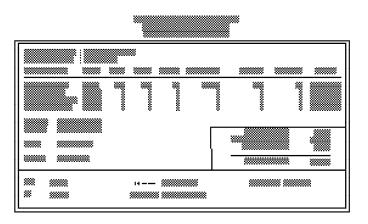
Press <Ctrl> <Alt> <Esc> or <Del> simultaneously to enter the Setup utility. A screen similar to the one below will appear.



Use the arrow keys to highlight the option you want and press <Enter>.

# Standard CMOS Setup

Use the arrow keys to highlight "Standard CMOS Setup" and press <Enter>, a screen similar to the one on the next page will appear.



#### Date and Time

Sets the time and date for the system. Press <F3> for the calendar.

# Primary Master, Primary Slave, Secondary Master and Secondary Slave

If you have added an IDE drive, you must select the appropriate type for the drive. You may use the "IDE HDD Auto Detection" from the main CMOS setup menu or you can set it manually. The G486VPB has 46 pre-set types and one user-definable type. Use the <Page Up> or <Page Down> keys to select the appropriate type for the drive.

The table in Appendix G gives a complete listing of the available drive types. Any given drive must be set to one specific drive-type number. Please refer to your IDE drive documentation to find the appropriate type number.

#### PCI/ISA System Board-

If none of the pre-set types is appropriate for your drive, choose "User", which is the user-definable type. To use this type, highlight either the drive you want to set: Primary Master, Primary Slave, Secondary Master or Secondary Slave. Use the arrow keys until type "User" is showing. Fill in all the parameters as specified by the drive manufacturer. If a drive is not installed, select "None" and press <Enter>.

#### Drive A and Drive B

These options are used to select the type of floppy disk drives installed in your system. If neither drive is present, select "None". Make sure you choose the correct drive type; otherwise, your system might format the device improperly.

#### Video

This is used to select the type of video adapter installed in your system.

### Halt on

The system will halt if an error is detected during power up.

**No Errors:** The system boot will not stop for any detected errors.

All Errors: The system will stop whenever the BIOS detects a non-fatal error.

**All, But Keyboard:** The system will stop for any errors except a keyboard error.

**All, But Diskette:** The system will stop for any errors except a disk error.

**All, But Disk/Key:** The system will stop for any errors except a keyboard or disk error.

#### Memory

The lower right hand corner shows the base memory size, extended memory size, and the other memory size of your system. You cannot alter these items; your computer automatically detects and displays them.

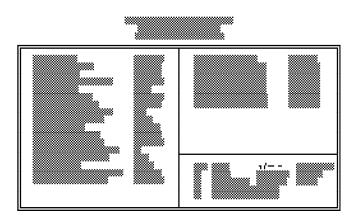
The Other Memory size refers to the memory located in the 640K to 1024K address space. This is the memory used for different applications. DOS uses this area to load device drivers to keep as much base memory free for application programs.

When you are through making changes in the Standard CMOS Setup, press <Esc> to return to the main menu.

## **BIOS Features Setup**

Use the arrow keys to highlight "BIOS Features Setup" and press <Enter>, a screen similar to the one on the next page will appear.





The Virus Warning option may be set to "enabled" or "disabled". When enabled, the BIOS issues a warning when any program or virus sends a Disk Format command or attempts to write to the boot sector of the hard disk drive.

If you choose "System" in the Security Option, you will be prompted for a password every time you cold boot your system or access setup. If you choose "Setup", you will be prompted for a password only when trying to access setup.

If the changes you made are incorrect or you change your mind, press <F6> or <F7> to return to the default settings. Press <Esc> after making the changes to return to the main menu.

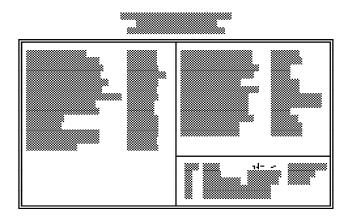




# Chipset Features Setup

The G486VPB uses the VIA chipset. The Chipset Features Setup allows you to modify some functions to optimize system performance. It also allows you to enable, disable or select the port address of the built-in serial ports, parallel port, floppy disk controller and hard disk controller.

If you press <Enter>, a screen similar to the one below will appear.

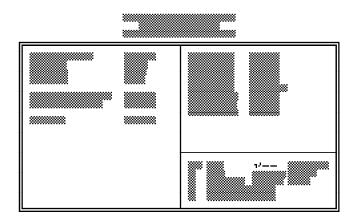


Use the arrow keys to move the highlight bar to the option you wish to change or modify. Use the <Page Up>, <Page Down>, <+> or <-> keys to make the corresponding changes.

If the changes you made are incorrect or you change your mind, press <F6> or <F7> to return to the default settings. Press <Esc> after making the changes to return to the main menu.

# Power Management Setup

Use the arrow keys to highlight "Power Management Setup" and press <Enter>. A screen similar to one below will appear

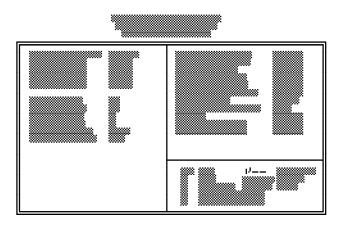


Choosing "Enabled" in the Power Management option will allow you to set Doze Timer, Sleep Timer and Sleep Mode. This will enable your system to reach one of the following power saving stages: Ready, Doze or Suspend. Choose "Disabled" if you do not want your system to enter the power saving mode.

G486VPB

# PCI Configuration Setup

Use the arrow keys to highlight "PCI Configuration Setup" and press <Enter>, a screen similar to the one below will appear.



The G486VPB system board supports four PCI slots. Each slot may be assigned INT value if the card installed in the slot requires an interrupt. Each INT may then be assigned an IRQ value.

## Load BIOS Defaults

If, for some reason, the CMOS becomes corrupted, the system can be reconfigured with the default values stored in the ROM chips. The BIOS default values provide the slowest performance for your system. You should use these values only if you are having hardware problems.

Highlight this option on the main menu and press <Enter>. The message below will appear.

#### Load BIOS Defaults (Y/N)? N

Type "Y" and press <Enter> to load the BIOS setup default values. After pressing <Enter>, you will be returned to the main menu.

## Load Setup Defaults

The Setup default values are very stable and should be selected as standard values for your system.

Highlight this option on the main menu and press <Enter>. The message below will appear.

#### Load Setup Defaults (Y/N)? N

Type "Y" and press <Enter> to load the Setup default values. After pressing <Enter>, you will be returned to the main menu.

To configure your system for optimum performance, you must reset the options under the Chipset Features setup. You are advised to do so only under the instruction of a technical engineer, otherwise, failure may occur.

## Password Setting

If you want to set a password, make sure that the Security Option under the BIOS Features Setup is set to "System" or "Setup". Refer to the BIOS Features Setup option for more information.

Use the arrow keys to highlight the password Setting option and press <Enter>. The message below will appear.

## Supervisor Password

Set a supervisor's password with "System" option selected if you want to protect your system and setup from unauthorized entry.

If you set a supervisor's password when "Setup" option is selected under the BIOS Features Setup, only access to setup will be denied when incorrect password is entered. You will not be prompted for a password when you cold boot the system.

Use the arrow keys to highlight the "Supervisor Password" option and press <Enter>. The message below will appear.

#### **Enter Password:**

Type in the password. You are limited to eight characters. Type in a password that is eight characters long or shorter. When done, the message below will appear:

#### Confirm Password:

You are asked to verify the password. Type in exactly the same password. If you type in a wrong password,

#### PCMSA System Board-

you will be prompted to enter the correct password again. Otherwise, enter a new password.

To delete or disable the password function, simply press <Enter> instead of typing in a new password. Press the <Esc> key to return to the main menu.

If you forget your password, you should clear any previously set password by setting Jumper JP2 pins 2 and 3 to On for approximately two minutes. Please refer to page 41 for more information.

#### User Password

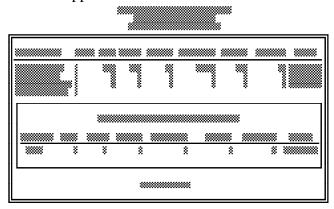
Set a user's password with "System" option selected if you want another user to have access only to your system but not to setup. Using user's password to enter Setup allows a user to access only "User Password" option that appears on the main screen. Access to all other options is denied.

If you want to set a user's password when "Setup" option is selected under the BIOS Features Setup, a user will be prompted to enter a password only when trying to access setup. If the correct password is entered, a user will be able to access only "User Password" option that appears on the main screen.

To set, confirm, verify, disable or delete a user's password, follow the procedure described in the section above, "Supervisor Password". If you forget your password, refer to the procedure described in the same section.

## IDE HDD Auto Detection

This option detects the hard disk parameters for the hard disk drives installed in your system. Highlight this option and press <Enter>. A screen similar to the one below will appear.



The screen displays the parameters detected and allows you to accept or reject the parameters. Type "Y" and press <Enter> to accept the parameters or press <Esc> to abort. If you select "Y", the parameters of the hard disk will be displayed in the Standard CMOS Setup.

## Save & Exit Setup

When all the changes have been made, highlight "Save & Exit Setup" and press <Enter>. The message below will appear:

## Save to CMOS and Exit (Y/N)? N

Type "Y" and press <Enter>. The following message will appear:

## Reboot System (Y/N)? N

Type "Y" and press <Enter>. The modifications you have made will be written into the CMOS memory, and the system will reboot. You will once again see the initial diagnostics on the screen. If you wish to make additional changes to the setup, press <Ctrl> <Alt> <Esc> simultaneously or <Del> after memory testing is done.

## Exit Without Saving

If you do not want to save the changes you have made, highlight this option and press <Enter>. The message below will appear:

## Quit Without Saving (Y/N)? N

Type "Y" and press <Enter>. The system will reboot and you will once again see the initial diagnostics on the screen. If you wish to make any changes to the setup, press <Ctrl> <Alt> <Esc> simultaneously or <Del> after memory testing is done.



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# ❖ IDE Device Drivers

The G486VPB system board uses the VT82C496G that support two IDE channels: 1Fx channel (Primary IDE Channel) and 17x channel (Secondary IDE Channel). The IDE device drivers should be installed under the "INSTALL.EXE" Utility.

The G486VPB comes with IDE driver diskette(s). The diskette includes drivers for Microsoft Windows 3.1 and 3.11, Windows NT 3.1 and 3.5, OS/2 2.x and 3.0, Novell NetWare 3.11 and 4.0x, SCO UNIX 3.2.4.

#### Note:

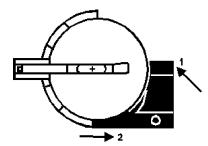
If you are using VT82C496G VLB IDE driver, Block Transfer Mode Option in the System BIOS must be turned off.

# Troubleshooting Checklist

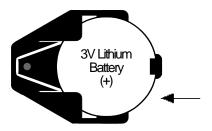
If you experience difficulty with the G486VPB system board, please refer to the checklist below. If you still cannot isolate the problem, please contact your dealer.

- 1) Check the jumper settings to ensure that the jumpers are properly set. If in doubt, refer to the "Board Configuration" section.
- 2) Verify that all SIM modules are seated securely into the bank sockets.
- 3) Make sure that the SIM modules are in the correct locations.
- 4) Check that all populated memory banks are filled with valid size SIM modules.
- 5) If your board fails to function, place the board on a flat surface and seat all socketed components (gently press each component into the socket).
- 6) If you made changes to the BIOS settings, re-enter setup and load the BIOS defaults.
- 7) Check if the internal battery needs to be replaced. The internal battery is located beside the keyboard connector.

You should replace the old battery with the type of lithium battery already used on your system board, 3V lithium battery CR2032. One of the battery sockets presented below is supported by your G486VPB board.



To remove the battery from the socket above, push the black button downward (1) and the battery to the right (2).



To remove the battery from the socket, simply push the battery to the left. It will slide out of the socket.

### **Caution:**

Battery may explode if mistreated. Do not recharge, disassemble or dispose of in fire.

### PCI/ISA System Board-

## Appendix A: Types of Modules

The G486VPB system board allows you to populate memory with 256Kx36, 512Kx36, 1Mx36, 2Mx36, 4Mx36, 8Mx36 and 16Mx36 SIM modules. The following modules have been tested with this board. Most untested brands will work but a few may fail to do so.

### For SIM 256Kx36 Modules

Brand	Chip Number
OKI	M51C256A-70

### For SIM 512Kx36 Modules

Brand	Chip Number
OKI	M514256B-70J

### For SIM 1Mx36 Modules

Brand	Chip Number
Toshiba	TC511000AJL
OKI	GT-1M*36B
Fujitsu	81C1000A-70





### For SIM 2Mx36 Modules

Brand	Chip Number
OKI	M511000B

### For SIM 4Wx36 Modules

Brand	Chip Number
OKI	M514100A-70S

### For SIM 8Mx36 Modules

Brand	Chip Number
OKI	M514100AL-70K

## Appendix B: System Error Report

When the BIOS encounters an error that requires the user to correct something, either a beep code will sound or a message will be displayed in a box in the middle of the screen and a message PRESS F1 TO CONTINUE, CTRL-ALT-ESC or DEL TO ENTER SETUP will be shown in the information box at the bottom.

### POST Beep

There is one beep code in BIOS. This code indicates that a video error has occurred and the BIOS cannot initialize the video screen to display any additional information. This beep code consists of a single long beep followed by two short beeps.

### Error Messages

One or more of the following messages may be displayed if the BIOS detects an error during the POST.

### **CMOS BATTERY HAS FAILED**

CMOS battery is no longer functional. It should be replaced. The replacement battery is a 3V lithium CR2032. Replacing the battery will clear the CMOS. Refer to the Jumper JP22 section of this manual, page 41, to reset the CMOS. Location of the battery and replacement procedure are given on pages 73 and 74 of the manual.

### CMOS CHECKSUM ERROR

Checksum of CMOS is incorrect. This can indicate that CMOS has become corrupt. This error may have been caused by a weak battery. Check the battery and replace if necessary.

### DISK BOOT FAILURE, INSERT SYSTEM DISK AND PRESS ENTER

No boot device was found. Insert a system disk into Drive A and press <Enter>. Assuming the system is booted from the hard drive, make sure the controller is inserted correctly and all cables are properly attached. Also, be sure the disk is formatted as a boot device; then reboot the system.

### DISKETTE DRIVES OR TYPES MISMATCH ERROR - RUN SETUP

Type of diskette drive installed in the system is different from the CMOS definition. Run setup to reconfigure the drive type correctly.

### DISPLAY SWITCH IS SET INCORRECTLY

Display switch on the motherboard can be set to either monochrome or color. This indicates the switch is set to a different setting than indicated in Setup. Determine which setting is correct, and then either turn off the system and change the jumper, or enter Setup and change the VIDEO selection.

### DISPLAY TYPE HAS CHANGED SINCE LAST BOOT

Since last powering off the system, the display adapter has been changed. You must configure the system for the new display type.



### ERROR ENCOUNTERED INITIALIZING HARD DRIVE

Hard drive cannot be initialized. Be sure the adapter is installed correctly and all cables are correctly and firmly attached. Also, be sure the correct hard drive type is selected in Setup (refer to Appendix G).

### ERROR INITIALIZING HARD DISK CONTROLLER

Cannot initialize controller. Make sure the card is correctly and firmly installed in the bus. Be sure the correct hard drive type is selected in Setup. Also, check to see if any jumper needs to be set correctly on the hard drive.

## FLOPPY DISK CNTRLR ERROR OR NO CNTRLR PRESENT

Cannot find or initialize the floppy drive controller. Make sure the controller is installed correctly and firmly. If there are no floppy drives installed, be sure the Diskette Drive selection in Setup is set to NONE.

### KEYBOARD ERROR OR NO KEYBOARD PRESENT

Cannot initialize the keyboard. Make sure the keyboard is attached correctly and no keys are being pressed during the boot.

If you are purposely configuring the system without a keyboard, set the error halt condition in Setup to HALT ON ALL, BUT KEYBOARD. This will cause the BIOS to ignore the missing keyboard and continue the boot.

### MEMORY ADDRESS ERROR AT...

Indicates a memory address error at a specific location. You can use this location along with the memory map for your system to find and replace the bad memory chips.

### MEMORY PARITY ERROR AT...

Indicates a memory parity error at a specific location. You can use this location along with the memory map for your system to find and replace the bad memory chips.

### MEMORY SIZE HAS CHANGED SINCE LAST BOOT

Memory has been added or removed since the last boot. Enter Setup and enter the new memory size in the memory fields.

### **MEMORY VERIFY ERROR AT...**

Indicates an error verifying a value already written to memory. Use the location along with your system's memory map to locate the bad chip.

### OFFENDING ADDRESS NOT FOUND

This message is used in conjunction with the I/O CHANNEL CHECK and RAM PARITY ERROR messages when the segment that has caused the problem cannot be isolated.

### OFFENDING SEGMENT

This message is used in conjunction with the I/O CHANNEL CHECK and RAM PARITY ERROR messages when the segment that has caused the problem has been isolated.

### PRESS A KEY TO REBOOT

This will be displayed at the bottom screen when an error occurs that requires a reboot. Press any key and the system will reboot.







### PRESS F1 TO DISABLE NMI, F2 TO REBOOT

When BIOS detects a Non-Maskable Interrupt condition during boot, this will allow NMI to be disabled and continue to boot. You can also reboot the system with the NMI enabled.

### RAM PARITY ERROR - CHECKING FOR SEGMENT

Indicates a parity error in Random Access Memory.

### SYSTEM HALTED, (CTRL-ALT-DEL) TO REBOOT...

Indicates the present boot attempt has been aborted and the system must be rebooted. Press and hold down the CTRL and ALT keys and press DEL simultaneously.

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## Appendix C: Memory & VO Maps

## Memory Address Map

Address	Name	Function
0000000 to 009FFFF	640K System Board RAM	System Board Memory
00A0000 to 00BFFFF	128K Video Display Memory	Reserved for Graphics Display Memory
00C0000 to 00E7FFF	160K I/O Expansion ROM	Reserved for ROM on I/O Adapter Card
00E8000 to 00FFFFF	96K ROM on the System Board	System Board BIOS
0100000 to 3FFFFFF	Maximum Memory 128M	System Board Memory

### VO Address Map

VO Address	Function
000-01F 020-03F	DMA Controller 1, 8237A-5 Interrupt Controller 1, 8259A, Master
040-05F	Timer, 8254-2
060-06F	8742 (Keyboard Controller)
070-07F	Real-time Clock, NMI
000.00	(Non-maskable Interrupt) Mask
080-09F 0A0-08F	DMA Page Memory, 74LS612
0C0-0DF	Interrupt Controller 2, 8259A DIMA Controller 2, 8237A-5
0E8	Shadow RAM and Cache Control Bit
0F0	Clear Numeric Processor
	Extension Busy
0F1	Reset Numeric Processor Extension
0F8-0FF	Numeric Processor Extension
1F0-1F8 200-207	Fixed Disk Game VO
200-207 278-27F	Parallel Printer Port 2
2F8-2FF	Serial Port 2
300-31F	Prototype Card
360-36F	Reserved
378-37F	Parallel Printer Port 1
380-38F	SDLC, Bisynchronous 2
3A0-3AF 3B0-3BF	Bisynchronous 1 Monochrome Display and Printer Adapte
3C0-3CF	Reserved
3D0-3DF	Color/Graphics Monitor Adapter
3F0-3F7	Diskette Controller
3F8-3FF	Serial Port 1

Note:
The I/O address hex 000 to 0FF are reserved for the system board I/0. Hex 100 to 3FF are available on the I/O channels.

## Appendix D: PCI VO Pin Assignments

-

В Solder Side **Component Side** A TRST# +12V TMS TDI +5V INTA# INTC# +5V Reserved +5V (I/O) Reserved Ground -12V
TCK
Ground
TDO
+5V
INTB#
INTDB#
INTDB#
PRSNT1#
Reserved
PRSNT2#
Ground
Ground
Ground
CLK
Ground
REQ#
+5V (I/O)
AD[31]
AD[29]
Ground
AD[27]
AD[25]
43.3V
C/BE[3]#
AD[17]
C/BE[2]#
Ground
AD[17]
C/BE[2]#
Ground
AD[17]
C/BE[2]# - 01 -- 01 -- 02 -- 03 -- 04 -- 05 -- 06 -- 07 -- 08 -- 09 -Ground Ground Reserved RST# +5V (I/O) GNT# Ground Reserved AD[30] +3.3V AD[28] AD[26] Ground AD[24] IDSEL +3.3V AD[20] Ground AD[18] AD[18] AD[16] +3.3V FRAME# Ground STOP# +3.3V SDONE SDONE SDONE SDONE SDONE 43.3V AD[15] +3.3V AD[15] +3.3V AD[15] 43.3V AD[15] AD[16] A +3.3V DEVSEL# DEVSEL#
Ground
LOCK#
PERR#
+3.3V
SERR#
+3.3V
C/BE[1]#
AD[14]
Ground
AD[12]
AD[10]
Ground - 48 -- 49 -AD[08] AD[07] +3.3V AD[05] AD[03] Ground AD[01] +5V (I/O) ACK64# +5V +5V C/BE[0]# +3.3V AD[06] AD[04] Ground AD[02] AD[00] +5V (I/O) REQ64# +5V +5V - 52 -- 53 -- 54 -- 55 -- 56 -- 57 -- 58 -- 59 -- 60 -- 61 -

## Appendix E: ISA IO Pin Assignments

<del>-</del>

Α

-l/OChck SD7 SD6 SD5 SD4 SD3 SD2 SD1 SD0

-l/OChrdy
-l/OCh

D

- 01 -- 02 -- 03 -- 04 -- 05 -- 06 -- 07 -- 08 -- 09 -

- 09 -- 10 -- 11 -- 12 -- 13 -- 14 -

- 16 -- 17 -

- 18 -

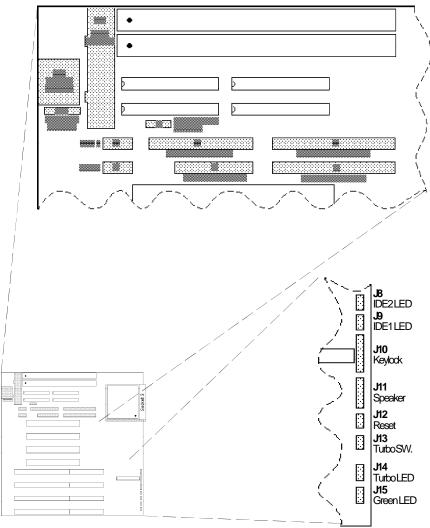
-MemCS16 -I/OCS16 IRQ10 IRQ11 IRQ12 IRQ13 IRQ14 -Dack0 DRQ0 -Dack5 DRQ5 -Dack6 DRQ6

-Dack7 DRQ7 +5VDC

-Master Gnd C
SBHE
LA23
LA21
LA20
LA19
LA18
LA17
-Memin
SD08
SD10
SD11
SD12
SD13
SD14
SD15



## Appendix F: Connector Pin Assignments



Locations of Connectors on the System Board

### PCI/ISA System Board —

Connectors J1 and J2 Primary (COM1) and Secondary (COM2) Serial Ports

Pin	Function
1 2 3 4 5 6 7 8 9	DCD (Data Carrier Detect) RX (Receive Data) TX (Transmit Data) DTR (Data Terminal Ready) Ground (Signal Ground) DSR (Data Set Ready) RTS (Request to Send) CTS (Clear to Send) RI (Ring Indicator)

# **Connector J5**Parallel Printer Port

Pin	Function
1 2 3 4 5 6 7 8 9	-Strobe Data 0 Data 1 Data 2 Data 3 Data 4 Data 5 Data 6 Data 7

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Pin	Function
10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25	-Adk Busy Paper Empty Select -Autofd -Error -Init -Slctin Ground Ground Ground Ground Ground Ground Ground

**Connector J4** Floppy Disk Drive Connector

Pin	Function
1 2 3 4 5 6 7 8	Ground DENSEL Ground Reserved Ground Drate0 Ground Index

### Ψ

### PCI/ISA System Board -

Pin	Function
9101112131415161718192021222242526728293332333	Ground MTR0 Ground DR1 Ground DR0 Ground MTR1 Drate1 Dir Ground Step Ground Write Data Ground Write Gate Ground Track 0 MSEN Wr Protect Ground Read Data Ground Head Select Ground Disk Change







**Connectors JP6 and JP7**Primary and Secondary IDE Hard Disk Drive Connectors

Pin	Function
1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23	-Reset Ground D7 D8 D6 D9 D5 D10 D4 D11 D8 D12 D2 D13 D1 D14 D0 D15 Ground Reserved Ground
24	-10W Ground
25	-IOR

### PCI/SA System Board -

Pin	Function
26 72 28 29 39 39 29 39 39 39 39 39 39 39 39 39 39 39 39 39	Ground IO Channel Ready* BALE Reserved Ground IRQ14 IOCS16 SA1 Reserved SA0 SA2 HCS0 HCS1 LED Ground

<sup>\*</sup> Pin 27 is Reserved for Secondary IDE Channel

# Connectors J8 and J9 IDE1/IDE2 LED

Pin	Function
1	Signal
2	LED Power

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### Connector J10 Keylock Connector

Pin	Function
1	LED Signal
2	Reserved
3	Ground
4	Keylock Signal
5	Ground

# **Connector J11** Speaker Connector

**Function** Pin Signal Reserved Ground 1 2 3 4 +5V

# **Connector JP12**Reset Switch Connector

Pin	Function
1	Ground
2	Reset

### PCI/ISA System Board —

### Connector J13

Turbo/Green Button Connector

Pin	Function
1	GND
2	Signal

**Connector J14**Turbo LED Connector

Pin	Function
1 2	Signal LED Power

**Connector J15** Green LED Connector

Pin	Function
1	Signal
2	LED Power

**Connector J3** External Battery Connector

Pin	Function
1	3.6 Lithium Battery
2	Key
3	GND
4	GND

**Connector CN2** Keyboard Connector

Pin	Function
1	Keyboard Clock
2	Keyboard Data
3	Not Connected
4	Ground
5	+5V

**Connector CN3** PS/2 Mouse Connector

Pin	Function
1	Mouse Data
2	Reserved
3	Ground
4	+5V
5	Mouse Clock
6	Reserved



# **Connector PL1**Power Connector

Pin	Function
1	Power Good
2	+5V
3	+12V
4	-12V
5	Ground
6	Ground
7	Ground
8	Ground
9	-5V
10	+5V
11	+5V
12	+5V

## Appendix G: Award BIOS Hard Disk Table

Туре	Size (MB)	Cylinders	Heads	Sectors	Write Precomp	Land Zone	Example Model
1	10	306	4	17	128	305	TEAC SD510, MMI 112, 5412
2	20	615	4	17	300	615	Seagate ST225, ST4026
3 4 5	30 62 46	615 940 940	6 8 6	17 17 17	300 512 512	615 940 940	OT IOLO
6	20	615	4	17	None	615	Seagate ST125, Tandon TM262
7 8 9 10	30 112 20	462 733 900 820	8 5 15 3 5	17 17 17 17	256 None None None	511 733 901 820	Tandon TM 703
11 12 13	35 49 20	855 855 306	5 7 8	17 17 17	None None 128	855 855 319	Disctron 526, MMI M125
14 15	42	733	7	17 (Reserved	None 1)	733	IVIIVII IVIIZO
16	20	612	4	` 17	0	663	Microscience HH725, Syquest 3250, 3425
17	40	977	5	17	300	977	,
18	56	977	7	17 47	None	977	
19 20	59 30	1024 733	7 5	17 17	512 300	1023 732	
21	42	733	5 7	17	300	732	
22	30	306	5	17	300	733	Seagate ST4038
23	10	977	4	17	0	336	Coagaio C1 1000
24	40	1024	5	17	None	976	Seagate ST4051
25	76	1224	9	17	None	1023	Seagate ST4096
26	71	1224	7	17	None	1223	Maxtor 2085
27	111	1224	11	17	None	1223	Maxtor 2140, Priam S14
28 	152	1024	15 	17	None	1223	Maxtor 2190, Priam S19

### PCVISA System Board -

Туре	Size (MB)	Cylinders	Heads	Sectors	Write Precomp	Land Zone	Example Model		
29	68	1024	8	17	None	1023	Maxtor 1085, Micropolis 1325		
30	93	918	11	17	None	1023	Maxtor 1105 1120, 4780		
31	83	925	11	17	None	1023	Maxtor 1170		
32	69	1024	9	17	None	926	CDC 9415		
33	85	1024	10	17	None	1023			
34	102	1024	12	17	None	1023			
35	110	1024	13	17	None	1023			
36	119	1024	14	17	None	1023			
37	17	1024	2	17	None	1023			
38	136	1024	16	17	None	1023			
39	114	918	15	17	None	1023	Maxtor 1140, 4380		
40	40	820	6	17	None	820	Seagate ST251		
41	42	1024	5	17	None	1023	Seagate 4053 Miniscribe 3053/6053		
42	65	1024	5	26	None	1023	Miniscribe 3053/6053 RLL		
43	40	809	6	17	None	852	Miniscribe 3650		
44	61	809	6	26	None	852	Miniscribe 3675 RLL		
45	100	776	8	33	None	775	Conner CP3104		
46	203	684	16	38	None	685	Conner CP3204		
User Defined									

